

AMENDMENTS TO THE CLAIMS

1. (Currently Amended): An LCD device having a driving circuit and a pixel array,
comprising:

a plurality of pairs of signal lines formed at constant intervals between the driving circuit
and the pixel array, each pair of the signal lines including first and second signal lines;

a common electrode intersecting ~~arranged in a vertical direction to~~ the signal lines,
thereby forming first and second signal-line portions;

a plurality of first electrostatic protecting circuits located at the first signal-line portion
~~arranged at one side of the common electrode and located~~ between adjacent the corresponding
first and second signal lines ~~in pairs, wherein each of the first electrostatic protecting circuits is~~
electrically connected with the first signal line ~~to be connected with the first signal lines of the~~
~~respective pairs~~ and the common electrode; and

a plurality of second electrostatic protecting circuits located at the second signal-line
portion ~~arranged at the other side of the common electrode and located between the~~
corresponding first and second signal lines, wherein each of the second protecting circuits is
electrically connected with the second signal line ~~to be connected with the second signal lines of~~
~~the respective pairs~~ and the common electrode,

wherein the first and second electrostatic protecting circuits are substantially aligned with
each other on opposite side of the common line.

2. (Original): The LCD of claim 1, wherein the common electrode includes two first and
second lines, the first line being connected with the first electrostatic protecting circuit and the
second line being connected with the second electrostatic protecting circuit.

3. (Original): The LCD of claim 2, wherein the first and second electrostatic protecting circuits are arranged at both sides around the first and second lines.

4. (Currently Amended): An LCD comprising:

a plurality of signal lines formed at constant intervals;

a common electrode intersecting ~~arranged in a vertical direction to~~ the signal lines, thereby forming first and second signal-line portions;

a plurality of first electrostatic protecting circuits located at the first signal-line portion, the first electrostatic protecting circuits electrically connected with odd numbered signal lines and the common electrode ~~at one side of the common electrode;~~ and

a plurality of second electrostatic protecting circuits located at the second signal-line portion, the second electrostatic protecting circuits electrically connected with even numbered signal lines and the common electrode ~~at the other side of the common electrode,~~

wherein the first and second electrostatic protecting circuits are substantially aligned with each other on opposite side of the common line.

5. (Previously Presented): The LCD of claim 4, wherein each of the signal lines includes protrusions for the first and second electrostatic protecting circuits.

6. (Withdrawn): An LCD comprising:

a plurality of signal lines formed at constant intervals;

first and second common electrodes arranged at constant intervals in a vertical direction to the signal lines;

a plurality of first and second electrostatic protecting circuits arranged at both sides of the first common electrode between first and second signal lines of odd numbered pairs in adjacent first and second signal lines in pairs to be respectively connected with a pair of the first signal

line and the first common electrode and a pair of the second signal line and the first common electrode; and

a plurality of third and fourth electrostatic protecting circuits arranged at both sides of the second common electrode between the first and second signal lines of even numbered pairs to be respectively connected with a pair of the first signal line and the second common electrode and a pair of the second signal line and the second common electrode.

7. (Withdrawn): The LCD of claim 6, wherein the first and second electrostatic protecting circuits are arranged to cross the third and fourth electrostatic protecting circuits.

8. (Withdrawn): The LCD of claim 6, wherein the respective signal lines are curved in portions where a pair of the first and second electrostatic protecting circuits and a pair of the third and fourth electrostatic protecting circuits are arranged.

9. (Withdrawn): The LCD of claim 6, wherein the first common electrode includes two first and second lines, the first line being connected with the respective first electrostatic protecting circuit and the second line being connected with the respective second electrostatic protecting circuit.

10. (Withdrawn): The LCD of claim 6, wherein the second common electrode includes two first and second lines, the first line being connected with the respective third electrostatic protecting circuit and the second line being connected with the respective fourth electrostatic protecting circuit.

11. (Withdrawn): An LCD comprising:
a plurality of pairs of signal lines;
a common electrode that extends perpendicular to the pairs of signal lines;

a plurality of first electrostatic protecting circuits arranged on one side of said common electrode, each of said first electrostatic protecting circuits positioned between a corresponding pair of signal lines, and wherein each of said first electrostatic protecting circuits is connected between a first signal line of its corresponding pair and the common electrode; and

a plurality of second electrostatic protecting circuits arranged on an opposite side of the common electrode, wherein each of said second electrostatic protecting circuits is positioned between a corresponding pair of signal lines, and wherein each of said second electrostatic protecting circuits is connected between a second signal line of its corresponding pair and the common electrode.

12. (Withdrawn): The LCD of claim 11, wherein said common electrode includes a first line and a second line, wherein said first electrostatic protecting circuits connect to said first line, and wherein said second electrostatic protecting circuits connect to said second line.

13. (Withdrawn): The LCD of claim 12, wherein said first and second electrostatic protecting circuits are respectively arranged on opposite sides of the first and second lines.

14. (Withdrawn): The LCD of claim 11, wherein said pairs of signal lines are equally spaced.

15. (Withdrawn): The LCD of claim 11, wherein said signal lines are straight.

16. (Withdrawn): The LCD of claim 11, wherein said signal lines include protrusions.

17. (Withdrawn): The LCD of claim 16, wherein said protrusions are beveled.

18. (Withdrawn): The LCD of claim 11, further including driving circuitry and an LCD array, wherein said signal lines extend between said driving circuitry and said LCD array.

19. (Withdrawn): An LCD comprising:
a plurality of signal lines having odd numbered signal lines and even numbered signal lines;
a common electrode that extends perpendicular to said signal lines;
a plurality of first electrostatic protecting circuits on one side of the common electrode, wherein each of said first electrostatic protecting circuits connects between each of odd numbered signal line and the common electrode; and
a plurality of second electrostatic protecting circuits on an opposite side of the common electrode, wherein each of said second electrostatic protecting circuits connects between each of even numbered signal lines and the common electrode.

20. (Withdrawn): The LCD of claim 19, wherein said pairs of signal lines are equally spaced.

21. (Withdrawn): The LCD of claim 19, wherein said signal lines include protrusions.

22. (Withdrawn): The LCD of claim 21, wherein said protrusions are beveled.

23. (Withdrawn): The LCD of claim 19, further including driving circuitry and an LCD array, wherein said signal lines extend between said driving circuitry and said LCD array.

24. (Withdrawn): An LCD comprising:
a plurality of pairs of signal lines;

first and second common electrodes that extend perpendicular to said signal lines;

a plurality of pairs of first electrostatic protecting circuits, wherein each pair of said first electrostatic protecting circuits is arranged between a corresponding pair of signal lines, wherein the first electrostatic protecting circuits of each pair are arranged on opposite sides of the first common electrode, wherein one of the first electrostatic protecting circuits of each pair connects to one of the signal lines of the corresponding pair of signal lines, and wherein the other first electrostatic protecting circuit of each pair connects to the other signal line of the corresponding pair of signal lines; and

a plurality of pairs of second electrostatic protecting circuits, wherein each pair of said second electrostatic protecting circuits is arranged between a corresponding pair of signal lines, wherein the second electrostatic protecting circuits of a pair are arranged on opposite sides of the second common electrode, wherein one of the second electrostatic protecting circuits of a pair connects to one of the signal lines of the corresponding pair, and wherein the other second electrostatic protecting circuit of a pair connects to the other signal line of the corresponding pair.

25. (Withdrawn): The LCD of claim 24, wherein said pairs of first and second electrostatic protecting circuits are alternatingly arranged.

26. (Withdrawn): The LCD of claim 24, wherein said signal lines include protrusions.

27. (Withdrawn): The LCD of claim 27, wherein said protrusions are beveled.

28. (Withdrawn): The LCD of claim 24, wherein said first common electrode includes first and second lines, wherein one of the first electrostatic protecting circuits of a pair

connects to said first line, and wherein the other first electrostatic protecting circuit of a pair connects to said second line.

29. (Withdrawn): The LCD of claim 24, further including driving circuitry and an LCD array, wherein said signal lines extend between said driving circuitry and said LCD array.

30. (Withdrawn): The LCD of claim 19, wherein the first and second electrostatic protecting circuits are respectively arranged on opposite sides of the odd or even numbered signal line.